

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 244844US2S		SERIAL NO. New Application	
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT					
		Kazuhisa SAKIHAMA, et al.		FILING DATE		GROUP	
		Herewith					
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
Z. K.	AA	6,249,410	06/19/01	Ming-Dou KER, et al.	-----	-----	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION		
	AO				YES	NO	
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
Z. K.	AW	Albert Z.H Wang, "On-Chip ESD Protection for Integrated Circuits, An IC Design Perspective", page 103 (no date)					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner		Date Considered 09/09/2004					
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							